

Ph Free

RoHS Compliant

Features

- Wide frequency range
- Quick delivery is possible by PLL technology
- Realizing high reliability by using sealed crystal
- Stand-by function (ST) can be used for low current consumption applications
- Pin compatible with half size

Applications

- Digital Electronics

How to Order

KPEX07- 60.0000 C 5 1 B 00
 ① ② ③ ④ ⑤ ⑥ ⑦

- ① Type
- ② Output Frequency
- ③ Output Type (CMOS)
- ④ Supply Voltage (5.0V)
- ⑤ Frequency Tolerance
- ⑥ Symmetry/ Enable Function (40/ 60%, Stand-by)
- ⑦ Customer Special Model Suffix (STD Specification is "00")

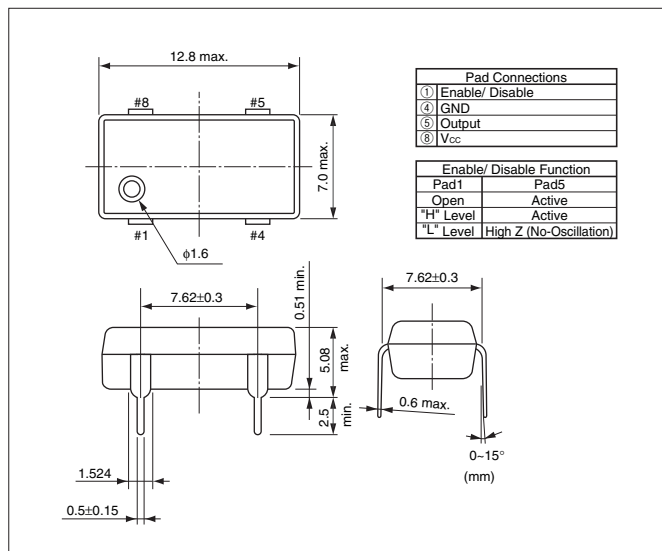
Specifications

Item	Symbol	Conditions	Specifications		Units
			Min.	Max.	
Output Frequency Range	f_o		1	125	MHz
Frequency Tolerance (Overall)	f_{tol}		-100	-100	$\times 10^{-6}$
Storage Temperature Range	T_{stg}		-55	+125	$^{\circ}\text{C}$
Operating Temperature Range	T_{use}		-10	+70	$^{\circ}\text{C}$
Supply Voltage	V_{cc}		4.5	5.5	V
Current Consumption	I_{cc}	$1 \leq f_o \leq 40$	—	25	mA
		$40 < f_o \leq 90$	—	35	mA
		$90 < f_o \leq 125$	—	40	mA
Stand-by Current	I_{std}		—	10	μA
Symmetry	SYM	@ 50% V_{cc}	40	60	%
Rise/ Fall Time	t_r / t_f	$1 \leq f_o \leq 10$	—	10	nS
		$10 < f_o \leq 60$	—	5	nS
		$60 < f_o \leq 125$	—	5	nS
Low Level Output Voltage	V_{OL}		—	10% V_{cc}	V
High Level Output Voltage	V_{OH}		90% V_{cc}	—	V
Input Voltage Range	V_{IN}		4.5	5.5	V
Low Level Input Voltage	V_{IL}		—	0.8	V
High Level Input Voltage	V_{IH}		2	—	V
CMOS Load	L_{CMOS}		—	15	pF
Disable Time	t_{dis}		—	100	nS
Enable Time	t_{ena}		—	5	mS
Start-up Time	t_{str}	@ Minimum operation voltage to be 0 sec.	—	5	mS

Note: All electrical characteristics are defined at the maximum load and operating temperature range.
 Because we use a PLL Technology, please be careful about the influence of the jitter enough.
 Please contact us for inquiry about supply Voltage 3.3V.

Dimensions

(Unit: mm)



Marking

